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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT(S)

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 Inventors : Glen Carey, et al.
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 Examiner :

U.S. PATENT DOCUMENTS

Examiner Initial	Patent Number	Patent Date	Name	Class/ Subclass	Filing Date
<u>JN</u>	2002-0054618	05/09/02	JIANG, et al.	372/50	
	6,327,293	12/04/01	SALOKATVE et al.	372/96	
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<u>JN</u>	3,649,382	03/24/72	HAWRYLO	148/171	

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class/ Subclass	Translation Yes <input checked="" type="checkbox"/> No <input type="checkbox"/>
<u>JN</u>	100 22 879	12/14/00	EPO		X

OTHER DOCUMENTS

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JN 1. B. Corbett, et al.: Low-stress hybridization of emitters, detectors and driver circuitry on a silicon motherboard for optoelectronic interconnect architecture, MATERIALS SCIENCE IN SEMICONDUCTOR PROCESSING, Vol. 3, No. 5-6, 2000, pp. 449-453

JN 2. R. Pu, et al.: Comparison of techniques for bonding VCSELs directly to Ics, JOURNAL OF OPTICS A: PURE AND APPLIED OPTICS, Vol. 1, No. 2, March 1999, pp. 324-329

JN 3. Bernd Jenichen, et al.: Crystal defects in vertical cavity epitaxial structures on GaAs investigated by X-ray methods, MATERIALS SCIENCE AND ENGINEERING B, ELSEVIER SEQUOIA, LAUSANNE, CH, Vol. 28, No. 1/3, 1 December 1994, pp. 520-522

EXAMINER

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DATE CONSIDERED

11/30/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.